

Fig.3

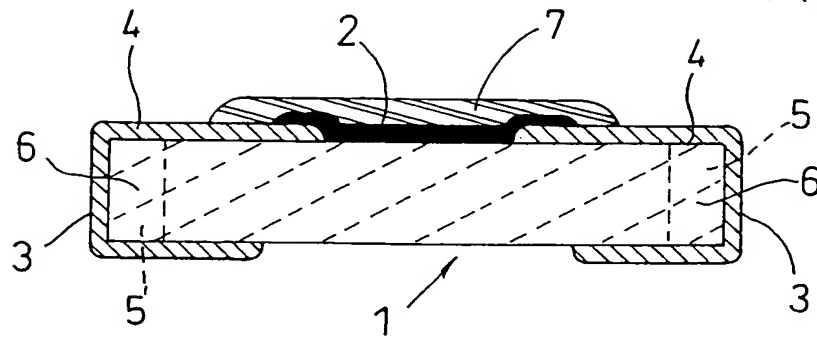
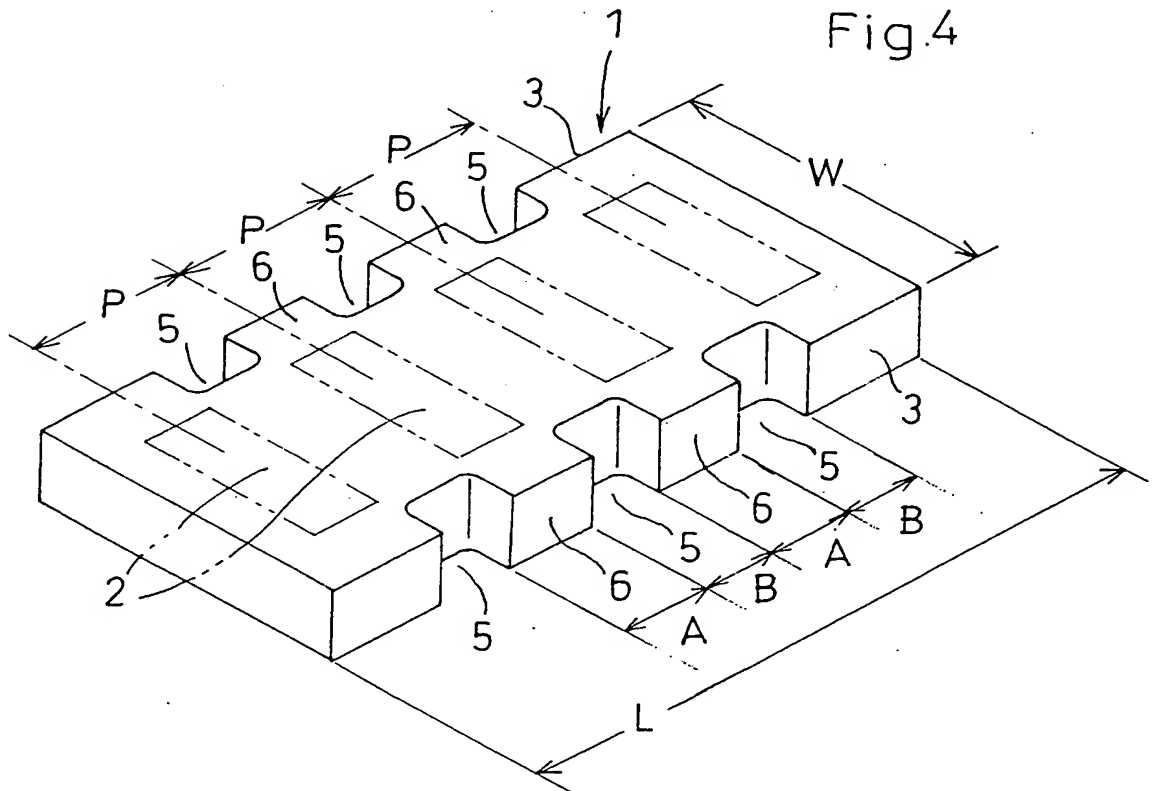


Fig.4



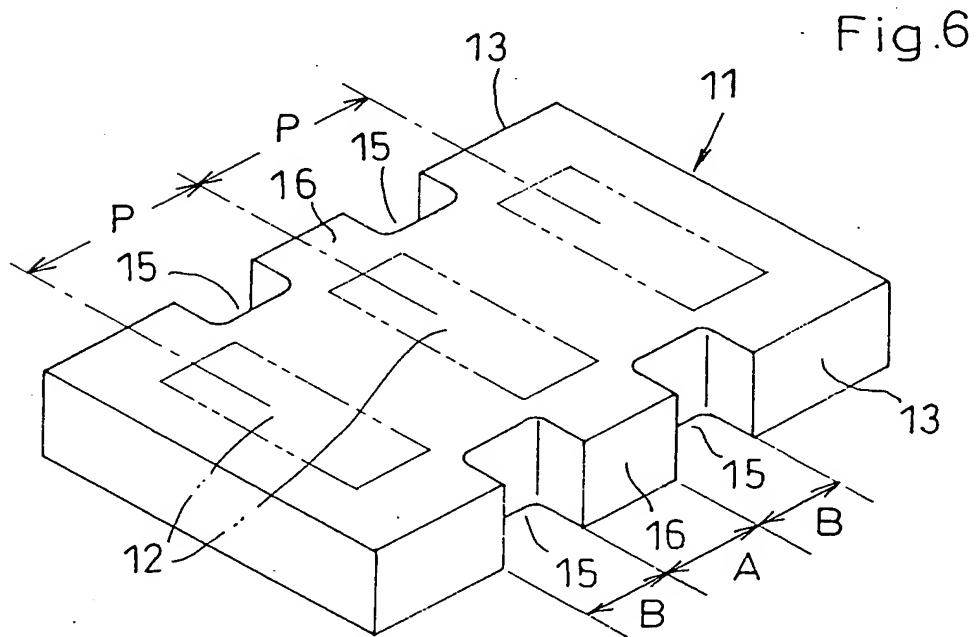
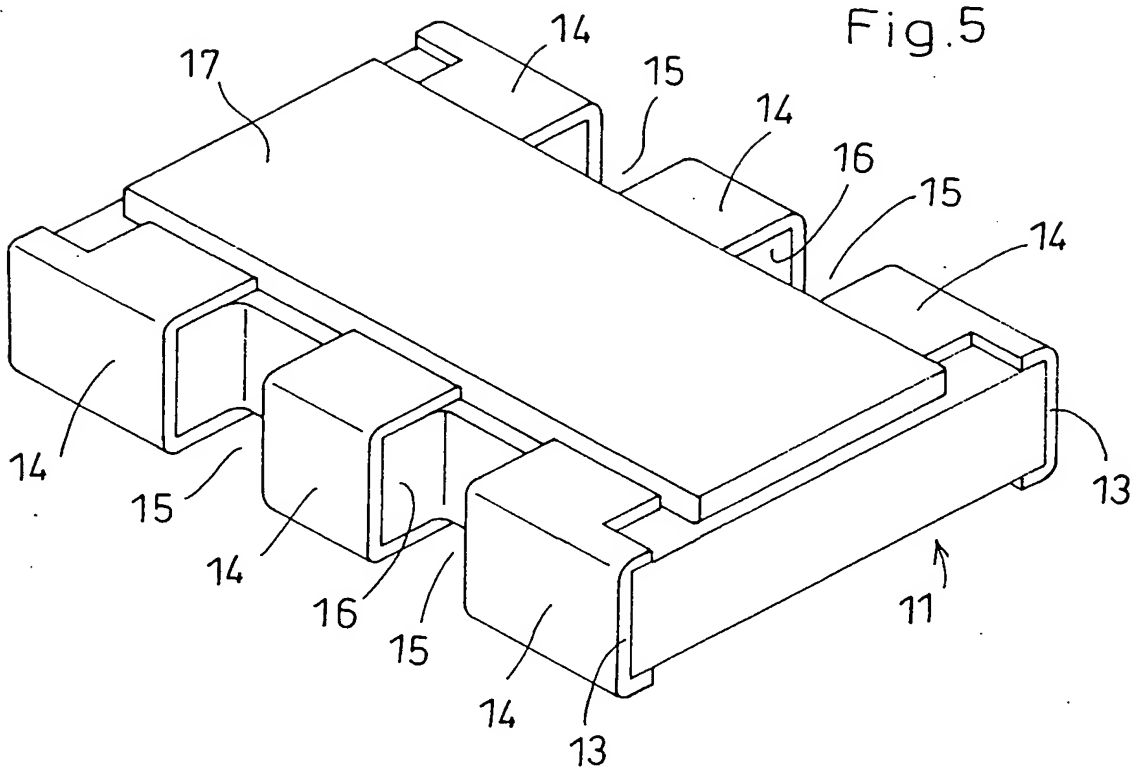


Fig.7

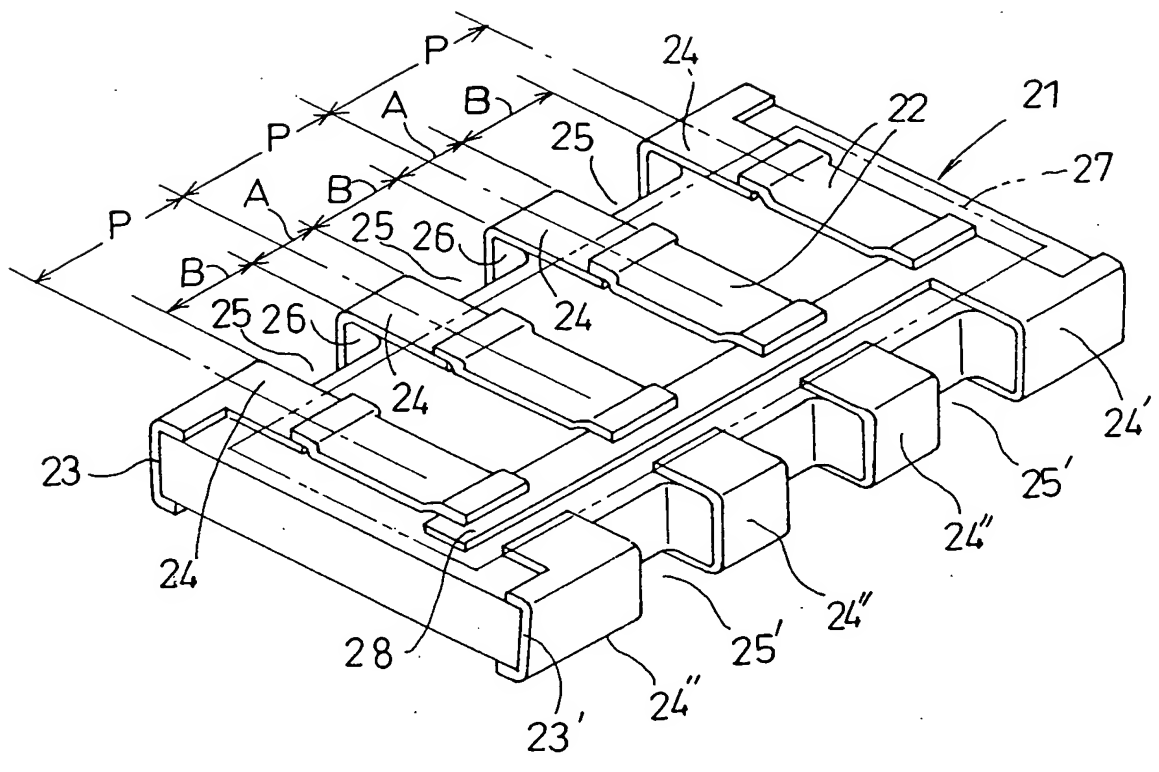




Fig. 9

Diagram illustrating a multi-layer printed circuit board (PCB) layout. The layout shows a grid of rectangular components (42) connected by horizontal and vertical traces (44, 45, 46). Dimensions are indicated: P (pitch), B (width), A (width), and C (height). A coordinate system X is shown at the top and bottom. Labels 47, 43, and 41 are also present.



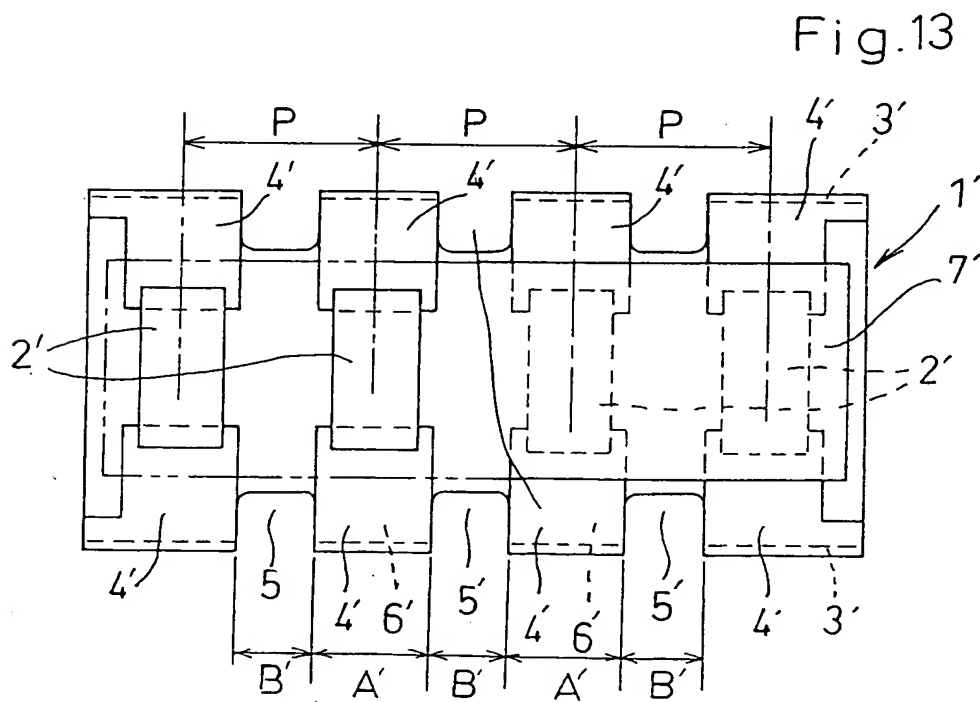
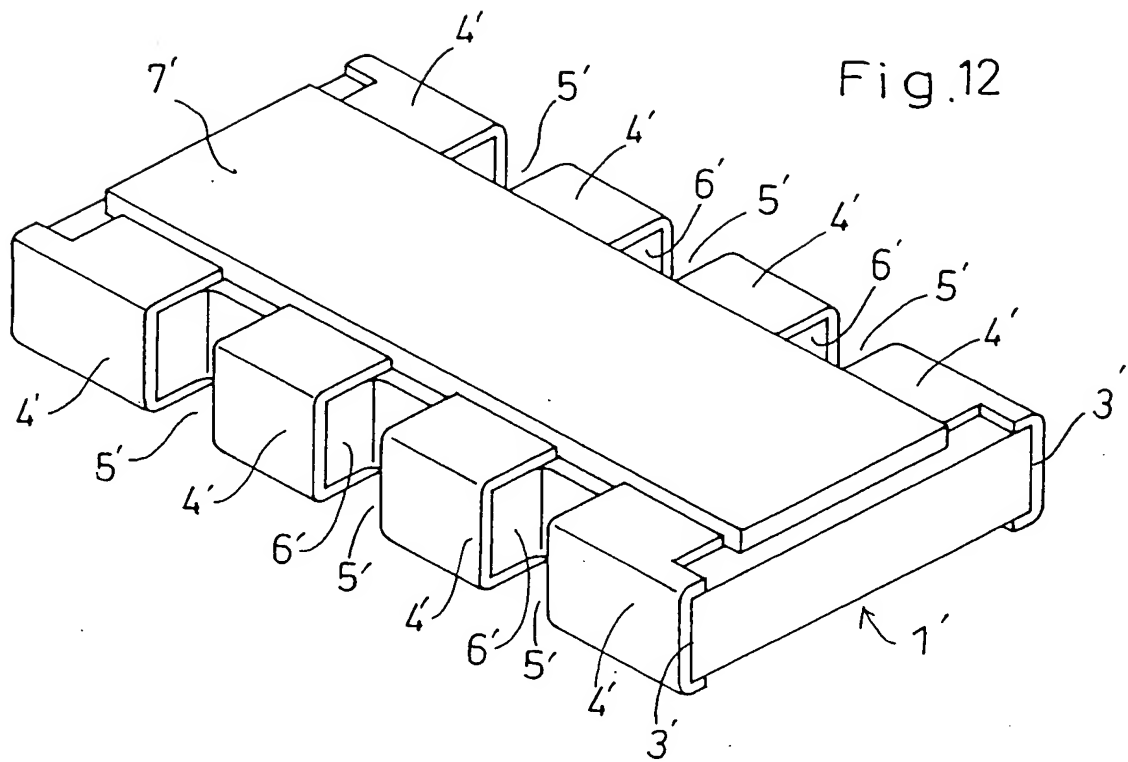




Fig.14

